55” LCD Preliminary Specification

CUSTOMER：_____

PARTNAME：_____

MODEL NO.：AML1550005

PART NO. ：_____

D A T E : 2018-03-23

SUPLLIER	
DRAFTER	APPROVER

USER	
DIRECTOR	APPROVER

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REVISION HISTORY

Version	Date	Page	Section	Description
Ver1.0	March 23rd 2018	ALL	ALL	AML1550005 Preliminary specification was first issued.

1. General Description

This specification applies to the 55 inch Color TFT-LCD SKD model T550HVN08.3. This Open Cell Unit has a TFT active matrix type liquid crystal panel 1,920x1,080 pixels, and diagonal size of 55 inch. This Open Cell Unit supports 1,920x1,080 mode. Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with an 8-bit gray scale signal for each dot.

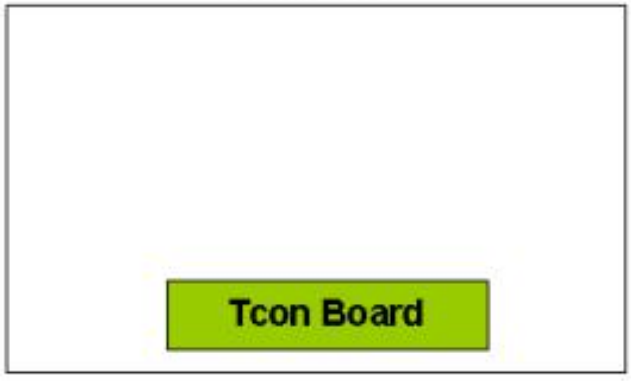
*** General Information**

Items	Specification	Unit	Note
Active Screen Size	55	inch	
Display Area	1209.6(H) x 680.4(V)	mm	
Outline Dimension	1219.6(H) x 726.88(V)	mm	
Cell Dimension	1219.6 (H) x 692.9 (V) x 1.3(D)	mm	D: cell thickness
Driver Element	a-Si TFT active matrix		
Display Colors	8 bit, 16.7M	Colors	
Number of Pixels	1,920x1,080	Pixel	
Pixel Pitch	0.21 (W) x 0.63(H)	mm	
Pixel Arrangement	RGB vertical stripe		
Display Operation Mode	Normally Black		
Surface Treatment	Anti-Glare, 3H		Haze=2%
Weight	Typ. 2530	g	
Rotate Function	Unachievable		Note 1
Display Orientation	Signal input with "ABC"		Note 2

Note 1: Rotate Function refers to LCD display could be able to rotate.

Note 2: LCD display as below illustrated when signal input with "ABC".

Rear side



Front side

2. Absolute Maximum Ratings

The followings are maximum values which, if exceeded, may cause faulty operation or damage to the unit

Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	V_{DD}	-0.3	14	[Volt] DC	Note 1
Input Voltage of Signal	Vin	-0.3	4	[Volt]DC	Note 1
Operating Temperature	TOP	0	+50	[°C]	Note 2
Operating Humidity	HOP	10	90	[%RH]	Note 2
Storage Temperature	TST	-20	+60	[°C]	Note 2
Storage Humidity	HST	10	90	[%RH]	Note 2
Panel Surface Temperature	PST		65	[°C]	Note 3
Electro Statistic Voltage	ESD		±2	[KV]	Note 4

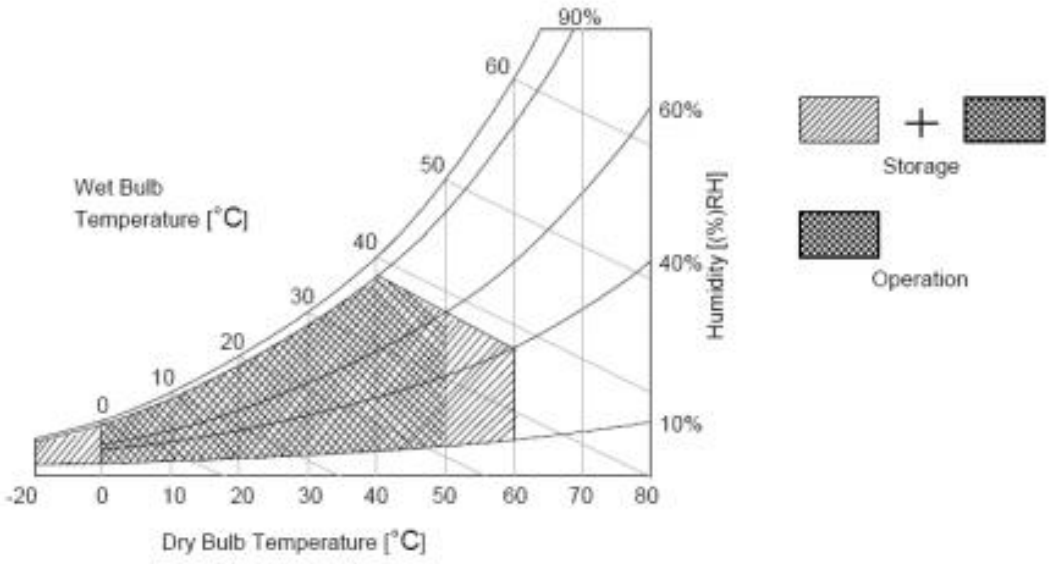
Note 1: Duration:50 msec.

Note 2 : Maximum Wet-Bulb should be 39℃ and No condensation.

The relative humidity must not exceed 90% non-condensing at temperatures of 40℃ or less. At temperatures greater than 40℃, the wet bulb temperature must not exceed 39℃.

Note 3: Surface temperature is measured at 50℃ Dry condition

Note 4: ESD protection procedure must be applied during production process; especially polarizer protection films remove process. Please directly contact AUO if module process advice is required.



3. Electrical Specification

The T550HVN08.1 Open Cell Unit requires power input which is employed to power the LCD electronics and to drive the TFT array and liquid crystal.

3.1 Electrical Characteristics

3.1.1 DC Characteristics

Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max		
LCD							
Power Supply Input Voltage		V_{DD}	10.8	12	13.2	V_{DC}	
Power Supply Input Current		I_{DD}	--	1.31	1.57	A	1
Power Consumption		P_C	--	15.72	18.97	Watt	1
Inrush Current		I_{RUSH}	--	--	2	A	2
Permissible Ripple of Power Supply Input Voltage		V_{RP}	--	--	V_{DD} * 5%	mV_{pk-pk}	3
LVDS Interface	Input Differential Voltage	$\lvert V_{ID} \rvert$	200	400	600	mV_{DC}	4
	Differential Input High Threshold Voltage	V_{TH}	+100	--	+300	mV_{DC}	4
	Differential Input Low Threshold Voltage	V_{TL}	-300	--	-100	mV_{DC}	4
	Input Common Mode Voltage	V_{ICM}	1.1	1.25	1.4	V_{DC}	4
CMOS Interface	Input High Threshold Voltage	V_{IH} (High)	2.7	--	3.3	V_{DC}	5
	Input Low Threshold Voltage	V_{IL} (Low)	0	--	0.6	V_{DC}	5

LED lightbar and LED Backlight structure are designed by customers, AUO can not guarantee life time and backlight power consumption.

3.1.2 AC Characteristics

Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max		
LVDS Interface	Input Channel Pair Skew Margin	$t_{SKEW\ (CP)}$	-500	--	+500	ps	6
	Receiver Clock : Spread Spectrum Modulation range	Fclk_ss	Fclk -3%	--	Fclk +3%	MHz	7
	Receiver Clock : Spread Spectrum Modulation frequency	Fss	30	--	200	KHz	7
	Receiver Data Input Margin Fclk = 85 MHz Fclk = 65 MHz	tRMG	 -0.4 -0.5	 -- --	 0.4 0.5	ns	8
I2C Interface	SCL clock frequency	F_{SCL}	0	--	400	KHZ	
	I2C clock high level	T_{SCHi}	0.6	--	--	us	
	I2C clock low level	T_{SCLo}	1.2	--	--	us	
	I2C data setup time	T_{SDS}	100	--	--	ns	
	I2C data hold time	T_{SDH}	0	--	900	ns	
	SDA and SCL rise time	T_R	--	--	1000	ns	
	SDA and SCL fall time	T_F	--	--	300	ns	

3.1.3 DRIVER CHARACTERISTICS

Item	Symbol	Min	Max	Unit	condition
Driver Surface Temperature	DST		100	[℃]	Note

Note : Any point on the driver surface must be less than 100℃ under any conditions.

3.1.4 TCON Characteristics

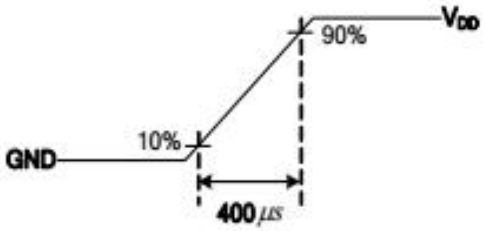
Item	Symbol	Min	Max	Unit	condition
TCON Surface Temperature	TST		85	[℃]	Note

Note : Any point on the TCON surface must be less than 85℃ under any conditions.

Note :

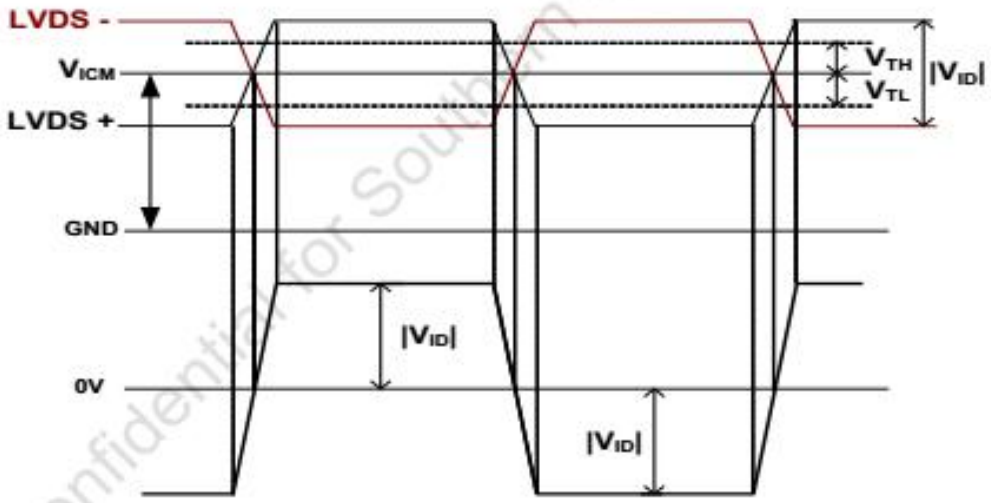
1. Test Condition:
   (1) V_{DD} = 12.0V
   (2) Fv = 60Hz
   (3) Fclk= Max freq.
   (4) Temperature = 25 ℃
   (5) Typ. Input current : White Pattern
   Max. Input current: Heavy loading pattern defined by AUO

2. Measurement condition : Rising time = 400us



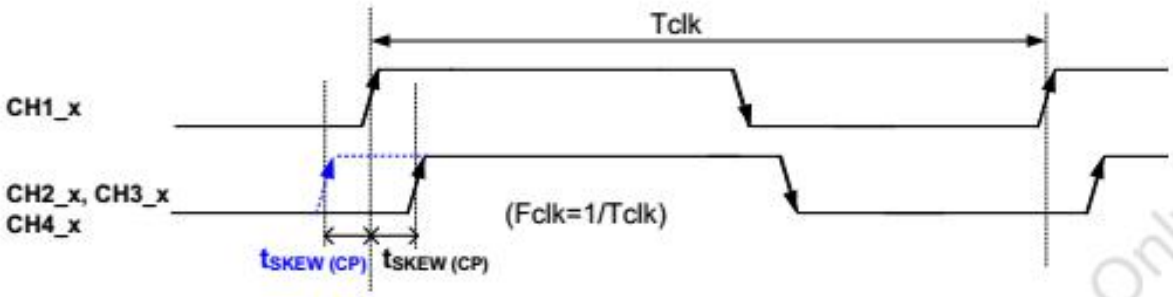
3. Test Condition:
   (1) The measure point of V_{RP} is in LCM side after connecting the System Board and LCM.
   (2) Under Max. Input current spec. condition.

4. V_{ICM} = 1.25V



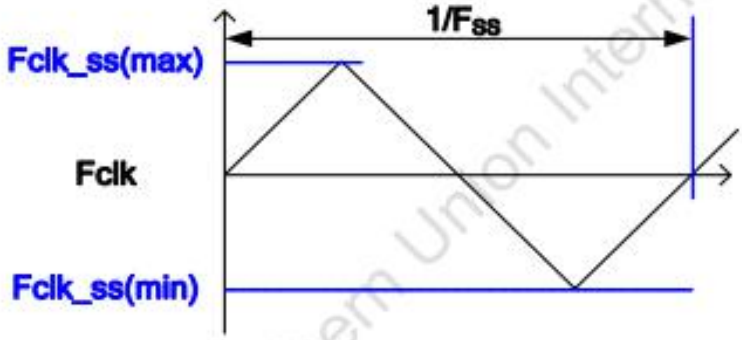
5. The measure points of V_{IH} and V_{IL} are in LCM side after connecting the System Board and LCM.

6. Input Channel Pair Skew Margin.



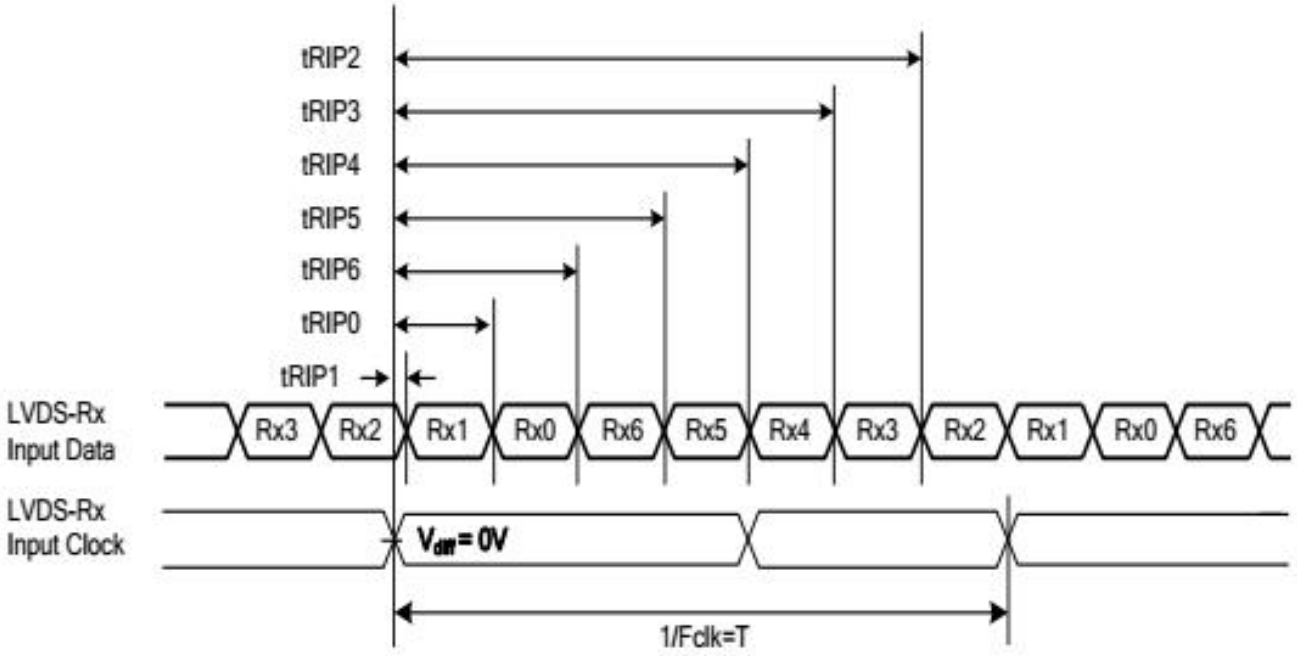
Note: x = 0, 1, 2, 3, 4

7. LVDS Receiver Clock SSCG (Spread spectrum clock generator) is defined as below figures.



8. Receiver Data Input Margin

Parameter	Symbol	Rating			Unit	Note
		Min	Type	Max		
Input Clock Frequency	Fclk	Fclk (min)	--	Fclk (max)	MHz	T=1/Fclk
Input Data Position0	tRIP1	-|tRMG|	0	|tRMG|	ns	
Input Data Position1	tRIP0	T/7-|tRMG|	T/7	T/7+|tRMG|	ns	
Input Data Position2	tRIP6	2T/7-|tRMG|	2T/7	2T/7+|tRMG|	ns	
Input Data Position3	tRIP5	3T/7-|tRMG|	3T/7	3T/7+|tRMG|	ns	
Input Data Position4	tRIP4	4T/7-|tRMG|	4T/7	4T/7+|tRMG|	ns	
Input Data Position5	tRIP3	5T/7-|tRMG|	5T/7	5T/7+|tRMG|	ns	
Input Data Position6	tRIP2	6T/7-|tRMG|	6T/7	6T/7+|tRMG|	ns	



3.2 Interface Connections

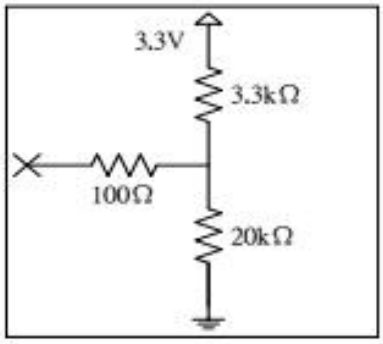
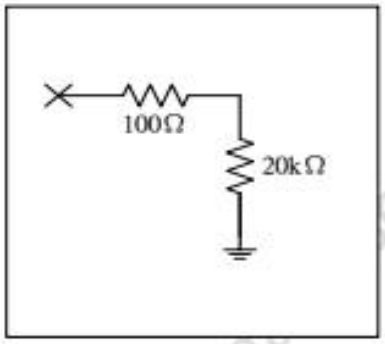
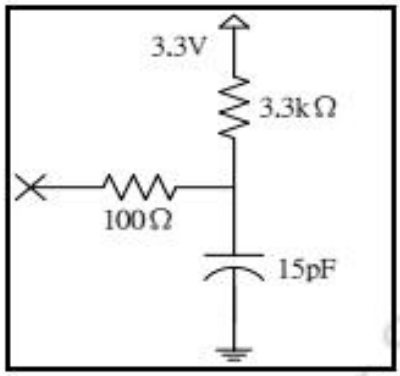
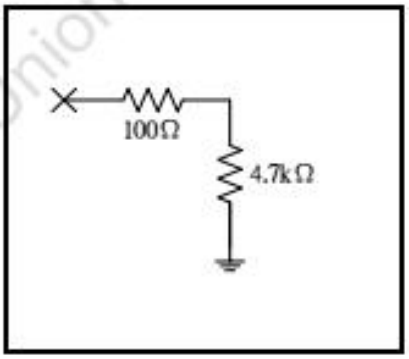
3.2.1 T-Con Board Pin Map

- LCD connector: P-TWO INDUSTRIES INC 187059-5122(LVDS C/N)
- LCD connector: CHIEF LAND Electronic Co.,Ltd.;115E51-0000RA-M3-R(LVDS C/N)

PIN	Symbol	Description	PIN	Symbol	Description
1	N.C.	No connection	26	N.C.	No connection
2	SCL	EEPROM Serial Clock	27	N.C.	No connection
3	WP	EEPROM Write Protection High(3.3V) for Writable, Low(GND) for Protection	28	CH2_0-	LVDS Channel 2, Signal 0-
4	SDA	EEPROM Serial Data	29	CH2_0+	LVDS Channel 2, Signal 0+
5	N.C.	No connection	30	CH2_1-	LVDS Channel 2, Signal 1-
6	N.C..	No connection	31	CH2_1+	LVDS Channel 2, Signal 1+
7	LVDS_SEL	Open/High(3.3V) for NS, Low(GND) for JEIDA	32	CH2_2-	LVDS Channel 2, Signal 2-
8	N.C.	No connection	33	CH2_2+	LVDS Channel 2, Signal 2+
9	N.C.	No connection	34	GND	Ground
10	N.C.	No connection	35	CH2_CLK-	LVDS Channel 2, Clock -
11	GND	Ground	36	CH2_CLK+	LVDS Channel 2, Clock +
12	CH1_0-	LVDS Channel 1, Signal 0-	37	GND	Ground
13	CH1_0+	LVDS Channel 1, Signal 0+	38	CH2_3-	LVDS Channel 2, Signal 3-
14	CH1_1-	LVDS Channel 1, Signal 1-	39	CH2_3+	LVDS Channel 2, Signal 3+
15	CH1_1+	LVDS Channel 1, Signal 1+	40	N.C.	No connection (for AUO test only)
16	CH1_2-	LVDS Channel 1, Signal 2-	41	N.C.	No connection (for AUO test only)
17	CH1_2+	LVDS Channel 1, Signal 2+	42	N.C.	No connection
18	GND	Ground	43	N.C.	No connection
19	CH1_CLK-	LVDS Channel 1, Clock -	44	GND	Ground
20	CH1_CLK+	LVDS Channel 1, Clock +	45	GND	Ground
21	GND	Ground	46	GND	Ground
22	CH1_3-	LVDS Channel 1, Signal 3-	47	N.C.	No connection
23	CH1_3+	LVDS Channel 1, Signal 3+	48	V_{DD}	Power Supply, +12V DC Regulated
24	N.C.	No connection (for AUO test only)	49	V_{DD}	Power Supply, +12V DC Regulated
25	N.C.	No connection (for AUO test only)	50	V_{DD}	Power Supply, +12V DC Regulated
			51	V_{DD}	Power Supply, +12V DC Regulated

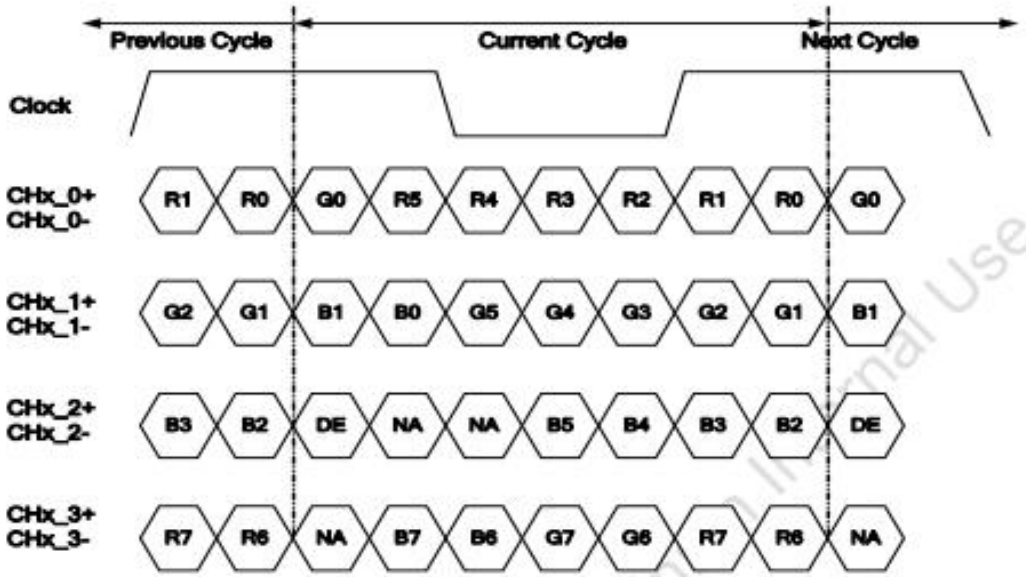
Note: N.C. : please leave this pin unoccupied. It can not be connected by any signal (Low/GND/High). Note: Open / High(3.3V) / Low(GND)/ WP / SDA / SCL described in 3.2.1.3

LVDS connector control pin description

Note * : Open/High(3.3V) 	**Note ** : Open/Low(GND)** 
Note ** : SCL/SDA** 	**Note ***** : WP** 

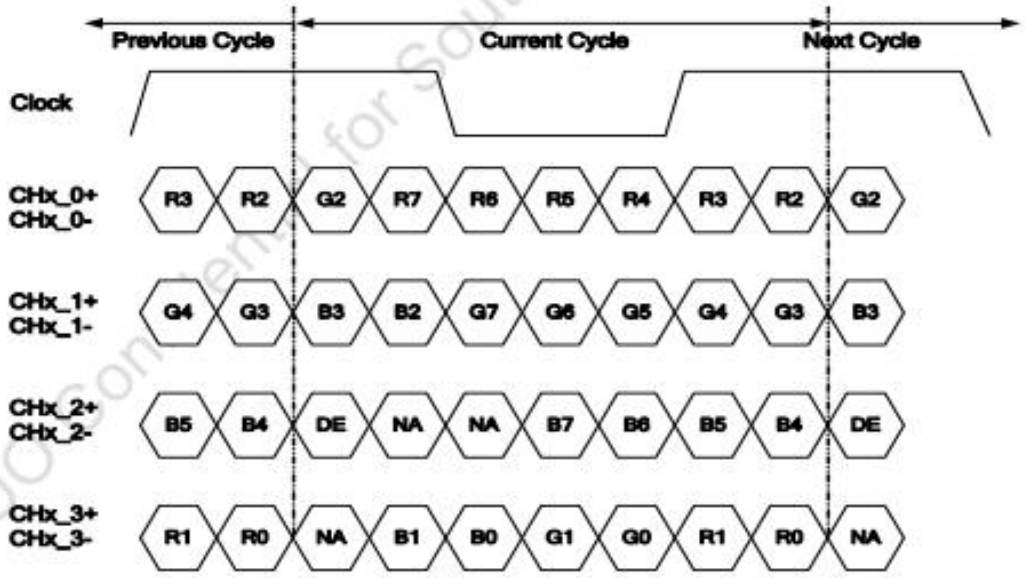
3.2.2 LVDS Option

LVDS Option = High/Open➔NS (for 8 bit)



Note: x = 1, 2, 3, 4...

LVDS Option = Low➔JEIDA



Note: x = 1, 2, 3, 4...

3.2.3 Signal Timing Specification

This is the signal timing required at the input of the user connector. All of the interface signal timing should be satisfied with the following specifications for its proper operation.

Timing Table (DE only Mode)

Signal	Item	Symbol	Min.	Typ.	Max	Unit
Vertical Section	Period	Tv	1100	1125	1480	Th
	Active	Tdisp (v)		1080		
	Blanking	Tblk (v)	20	45	400	Th
Horizontal Section	Period	Th	1030	1100	1325	Tclk
	Active	Tdisp (h)		960		
	Blanking	Tblk (h)	70	140	365	Tclk
Clock	Frequency	Fclk=1/Tclk	53	74.25	82	MHz
Vertical Frequency	Frequency	Fv	47	60	63	Hz
Horizontal Frequency	Frequency	Fh	60	67.5	73	KHz

Notes:

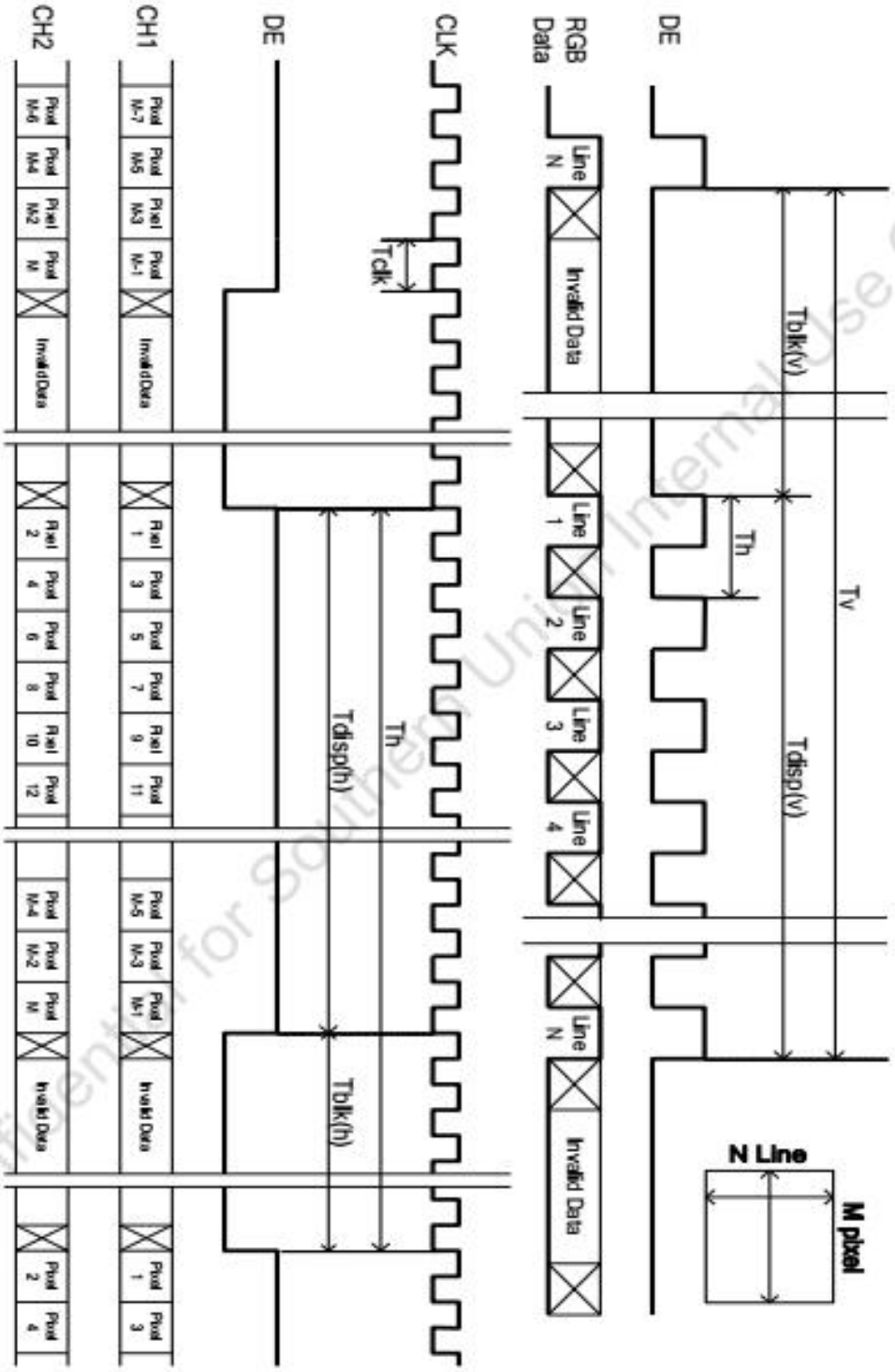
(1) Display position is specific by the rise of DE signal only.
Horizontal display position is specified by the rising edge of 1st DCLK after the rise of 1st DE, is displayed on the left edge of the screen.

(2)Vertical display position is specified by the rise of DE after a "Low" level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise of 1st DE is displayed at the top line of screen.

(3)If a period of DE "High" is less than 1920 DCLK or less than 1080 lines, the rest of the screen displays black.

(4)The display position does not fit to the screen if a period of DE "High" and the effective data period do not synchronize with each other.

3.3 Signal Timing Waveforms



3.4 Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 8 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

COLOR DATA REFERENCE

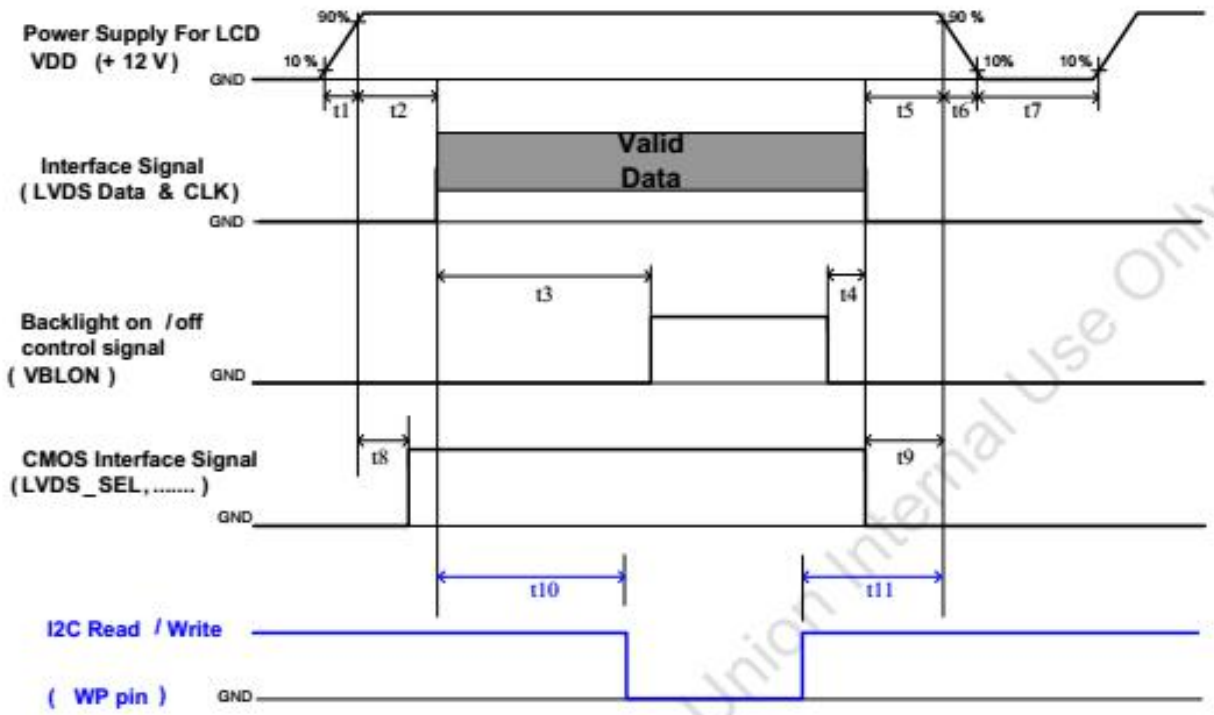
Color		Input Color Data																							
		RED								GREEN								BLUE							
		MSB							LSB	MSB							LSB	MSB							LSB
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
R	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

	RED(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
G	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0

	GREEN(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
B	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1

	BLUE(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

3.5 Power Sequence for LCD



Parameter	Values			Unit
	Min.	Type.	Max.	
t1	0.4	---	30	ms
t2	0.1	---	50	ms
t3	450	---	---	ms
t4	0 *1	---	---	ms
t5	0	---	---	ms
t6	---	---	--- *2	ms
t7	500	---	---	ms
t8	10 *3	---	50	ms
t9	0	---	---	ms
t10	450	---	---	ms
t11	150	---	---	ms

Note:

(1) t4=0 : concern for residual pattern before BLU turn off.

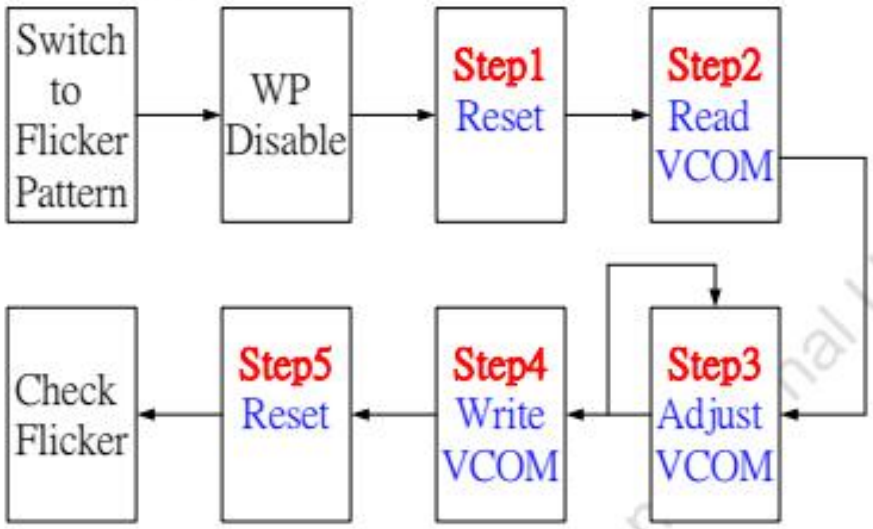
(2) t6 : voltage of VDD must decay smoothly after power-off. (customer system decide this value)

(3) When CMOS Interface signal is N.C. (no connection), opened in Transmitted end, t8 timing spec can be negligible.

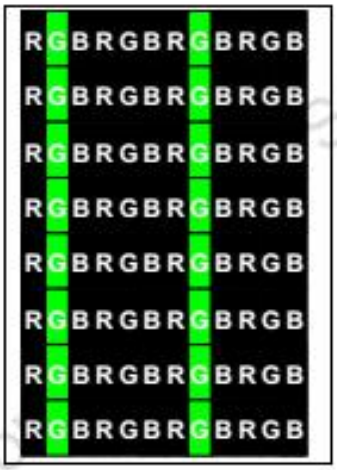
3.6 VCOM Adjust SOP

If you need below pattern or more detail information, please directly contact AUO for engineer service.

3.6.1 VCOM I2C Tuning Step



3.6.2 Flicker Pattern



3.6.3 WP (Write Protect) Disable

	Disable	Enable	Default (NC)
■	L	H	L

3.6.4 Adjust SOP

Step1 Reset

* Device Address is 0x74 (7Bits)

S	Slave Address	W	A	Index Address 0	A	Control Byte	A	P
	1 1 1 0 1 0 0 **0**			0 0 0 0 0 0 0 0		0 0 0 1 0 0 1 0		
	0xE8 Device Address + W			0x00 Control Address		0x12 Reset + OUT_EN		

Step2 Read VCOM

* Data = 7Bits

S	Slave Address	W	A	Index Address 1	A	S	Slave Address	R	A	DATA	NA	P
	1 1 1 0 1 0 0 **0**			0 0 0 0 0 0 0 1			1 1 1 0 1 0 0 **1**			X X X X X X X **X**		
	0xE8 Device Address + W			0x01 VCOM Address			0xE9 Device Address + R			Data		

Step3 Adjust VCOM

* DVCOM = 8Bits

S	Slave Address	W	A	Index Address 1	A	DVCOM	A	P
	1 1 1 0 1 0 0 **0**			0 0 0 0 0 0 0 1		0000000X~1111111X		
	0xE8 Device Address + W			0x01 VCOM Address		0x00~0xFF VCOM value		

Step4 Write VCOM

S	Slave Address	W	A	Index Address 0	A	Control Byte	A	P
	1 1 1 0 1 0 0 **0**			0 0 0 0 0 0 0 0		0 0 0 0 1 0 1 0		
	0xE8 Device Address + W			0x00 Control Address		0x0A Write DAC to NVM+ OUT_EN		

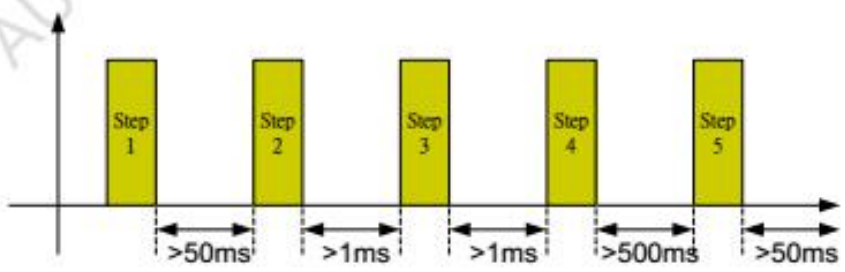
Step5 Reset

* Device Address is 0x74 (7Bits)

S	Slave Address	W	A	Index Address 0	A	Control Byte	A	P
	1 1 1 0 1 0 0 **0**			0 0 0 0 0 0 0 0		0 0 0 1 0 0 1 0		
	0xE8 Device Address + W			0x00 Control Address		0x12 Reset + OUT_EN		

3.6.5 Interval of Step to Step

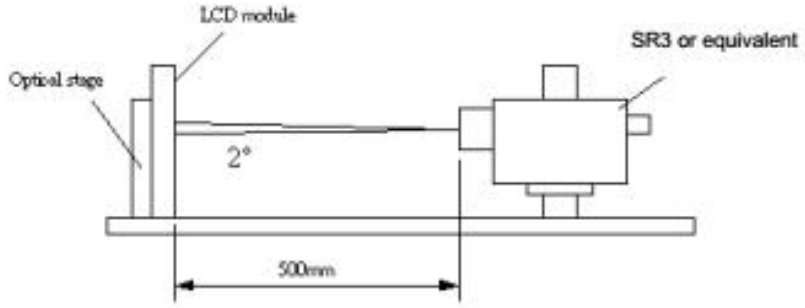
Step to step interval must follow below figure



4. Optical Specification

Optical characteristics are determined after the open cell unit and light source has been 'ON' and stable for approximately 45 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of φ and θ equal to 0°.

Fig.1 presents additional information concerning the measurement equipment and method.



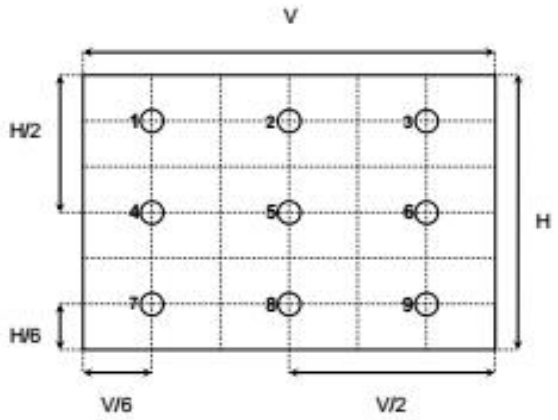
Parameter	Symbol	Condition	Values			Unit	Notes
			Min.	Typ.	Max		
Contrast Ratio	CR	With AUO Module	2800	3500	--		1, 2
White Variation	δ_{WHITE}		--	--	1.54		1, 3
Response Time (G to G)	T_Y		--	6.5	--	ms	4
Center Transmittance	T%			6.5		%	1, 7
Color Chromaticity		With CS-1000T Standard light source "C"					5
Red	R_X		Typ.-0.03	0.661	Typ.+0.03		
	R_Y			0.327			
Green	G_X			0.275			
	G_Y			0.590			
Blue	B_X			0.137			
	B_Y			0.117			
White	W_X			0.291			
	W_Y			0.344			
Viewing Angle		With AUO Module					1, 6
x axis, right(φ=0°)	θ_r		--	89	--	degree	
x axis, left(φ=180°)	θ_l		--	89	--	degree	
y axis, up(φ=90°)	θ_u		--	89	--	degree	
y axis, down (φ=270°)	θ_d		--	89	--	degree	

1. Light source here is the BLU of AUO Standard module.
2. Contrast Ratio (CR) is defined mathematically as:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance of } L_{on5}}{\text{Surface Luminance of } L_{off5}}$$

3. The white variation, δWHITE is defined as:

δ_{WHITE}= Maximum(L_{on1}, L_{on2},…,L_{on9})/ Minimum(L_{on1}, L_{on2},…L_{on9})



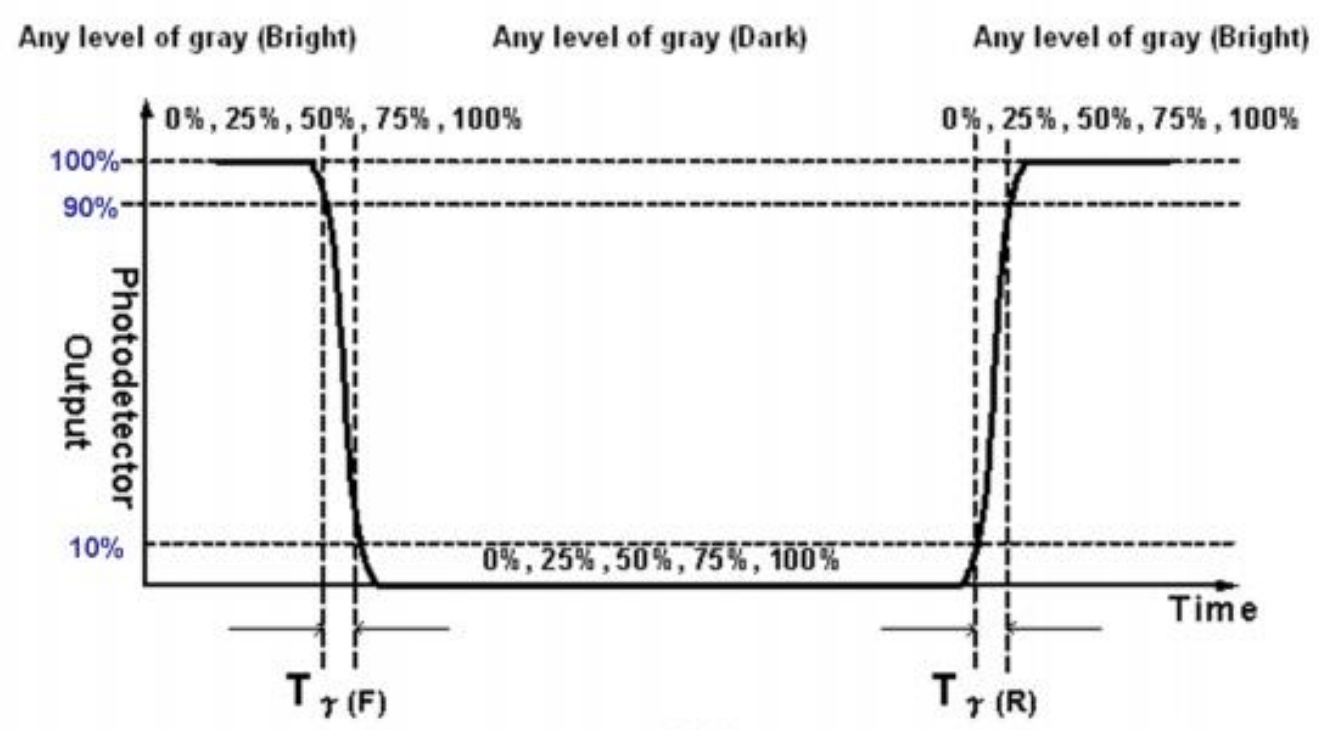
4. Response time T_r is the average time required for display transition by switching the input signal for five luminance ratio (0%,25%,50%,75%,100% brightness matrix) and is based on F_v=60Hz to optimize.

Measured Response Time		Target				
		0%	25%	50%	75%	100%
Start	0%		0% to 25%	0% to 50%	0% to 75%	0% to 100%
	25%	25% to 0%		25% to 50%	25% to 75%	25% to 100%
	50%	50% to 0%	50% to 25%		50% to 75%	50% to 100%
	75%	75% to 0%	75% to 25%	75% to 50%		75% to 100%
	100%	100% to 0%	100% to 25%	100% to 50%	100% to 75%	

T_γ is determined by 10% to 90% brightness difference of rising or falling period. (As illustrated)

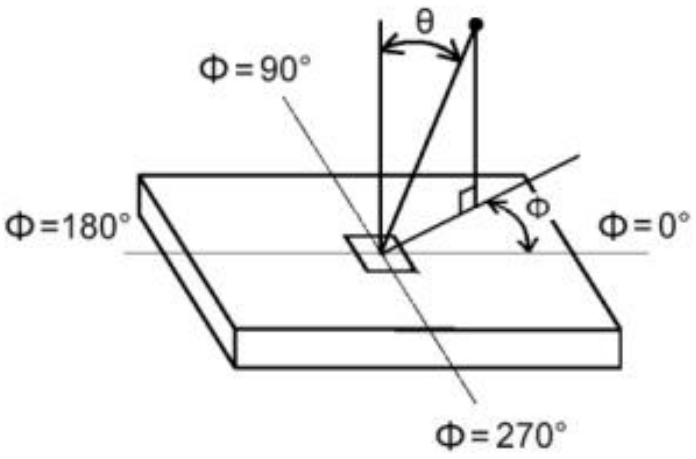
The response time is defined as the following figure and shall be measured by switching the input signal for "any level of gray(bright) " and "any level of gray(dark)".

FIG.3 Response Time



5. Light source here is the standard light source "C" which is defined by CIE and driving voltages are based on suitable gamma voltages. The calculating method is as following :
   A. Measure the "Module" and "BLU" optical spectrums (W, R, G, B).
   B. Calculate cell spectrum from "Module" and "BLU" spectrums.
   C. Calculate color chromaticity by using cell spectrum and the spectrum of standard light source "C".
6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG4.

FIG.4 Viewing Angle



7. Definition of Transmittance (T%):

$$\text{Transmittance} = \frac{\text{Luminance of LCD module}}{\text{Luminance of backlight}} * 100\%$$

During transmittance measurement, the backlight of LCD module contains no brightness enhancement film. Two diffuser sheets which diffuse the light source uniformly are suggested to use for transmittance measurement.

5. PTICAL CHARACTERISTICS

The relative measurement methods of optical characteristics are shown as below. The following items should be measured under the test conditions described in 6.1 and stable environment shown in Note (5).

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Contrast Ratio		CR	2800	3500	—	—	
Response Time	Variation	G to G Q	—	6.5	—	ms	
	Gray to gray	G to G BW	—	—	—		
Center Luminance of White		L_C		350			
White Variation		δW	—	—	—	-	
Transmittance		T		6.5		%	
Color Coordinates [CIE 1931]	Red	Rx	Typ. -0.03	TBD	Typ. +0.03	—	
		Ry				—	
	Green	Gx				—	
		Gy				—	
	Blue	Bx				—	
		By				—	
	White	Wx				—	
		Wy				—	
Viewing Angle （CR〉10）							
	X axis,right(Φ=0°）	θ_r	—	89	—	Deg.	
	X axis,left(Φ=180°）	θ_l	—	89	—		
	X axis,up(Φ=90°）	θ_u	—	89	—		
	X axis,down(Φ=270°）	θ_d	—	89	—		
Gray Scale			-	-	-		

Note (1) Definition of Viewing Angle (θx, θy):

Viewing angle are measured by CS-2000.

Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

Contrast Ratio (CR) = L255 / L0

L255: Luminance of gray level 255

L 0: Luminance of gray level 0

CR = CR (5), where CR (X) is corresponding to the Contrast Ratio of the point X at the figure in Note (6).

Note (3) Definition of Gray to Gray Switching Time:

The driving signal means the signal of luminance 0%,20%,40%,60%,80%,100%.

Gray to gray average time means the average switching time of luminance 0%,20%, 40%,60%,80%,100% to each other.

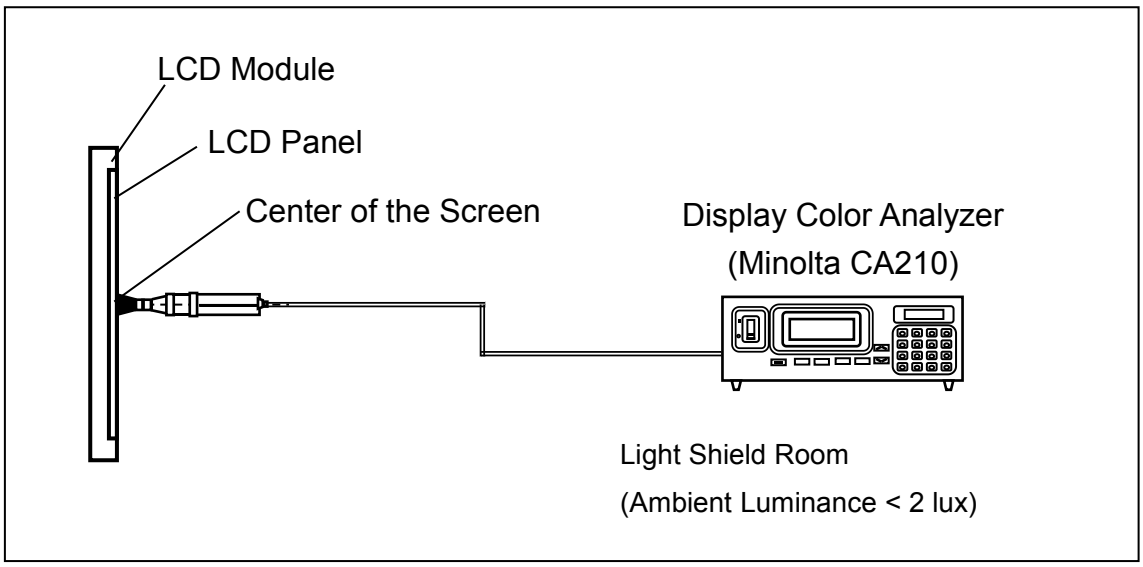
Note (4) Definition of Luminance of White (L_C):

Measure the luminance of gray level 255 at center point and 9 points

L_C = L (5), where L (X) is corresponding to the luminance of the point X at the figure in Note (6)

Note (5) Measurement Setup:

The LCD module should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a windless room.



Note (6) Definition of White Variation (δW) :

Measure the luminance of gray level 255 at 5 points

δW = Maximum [L (1), L (2), L (3), L (4), L (5)] / Minimum [L (1), L (2), L (3), L (4), L (5)]

5.1. BACKLIGHT UNIT

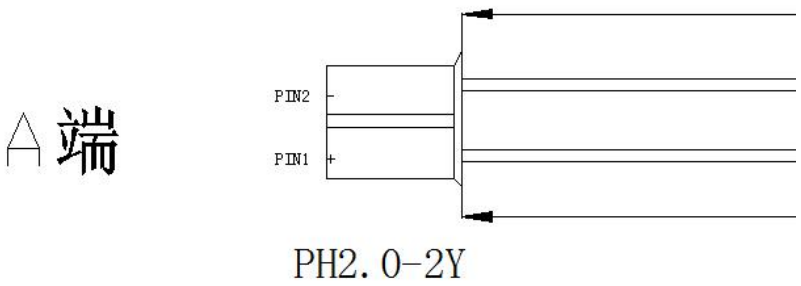
5.1..1 LED LIGHTBAR UNIT CHARACTERISTICS

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Lightbar input Voltage	V	96	99.2	102.4	V_{RMS}	One channel
Lightbar input current	I_L		400		mA	One channel
Power consume	W		39.68		W	One channel
Lightbar Life Time	L_{BL}	30000		-	Hrs	

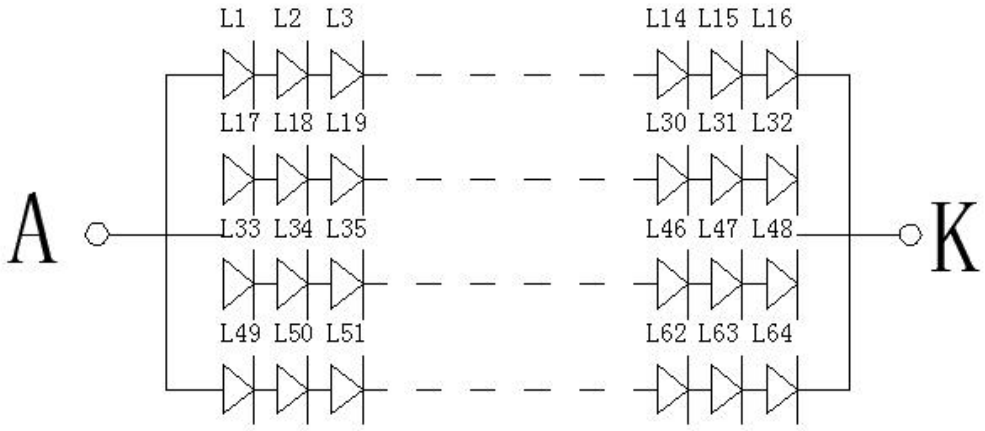
Note (1) The lifetime is defined as the time which luminance of the LED decays to 50% compared to the initial value, Operating condition: Continuous operating at Ta = 25±2℃ , IL=800mA.

The pin configuration for the housing and leader wire is shown in the table below.

Light bar connector type: (Cvilux)PH2.0-2PIN



The Light bar Diagram

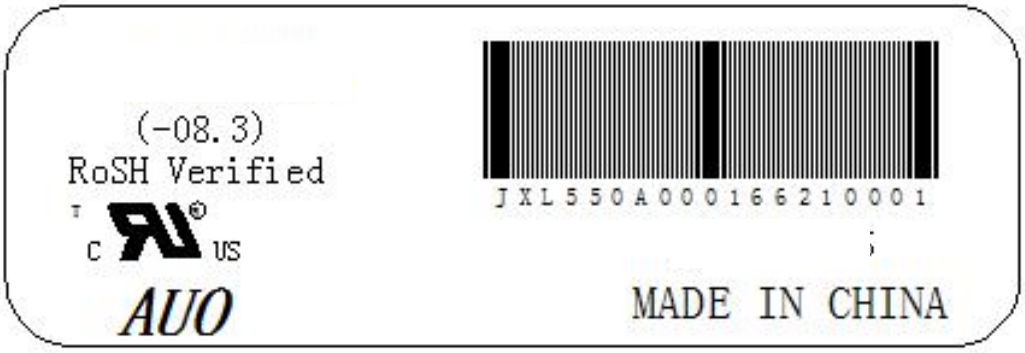


LED Numbers: 1p 产品使用 2pcs 灯条，1pcs 灯条使用 64pcs 灯珠，电路是 16 串 4 并

6. DEFINITION OF LABELS

6.1 LCD MODULE LABEL

The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.



条码代码

L----------产品系列，直下 D,侧入 L

550---------产品尺寸

A----------玻璃规格

000---------订单号

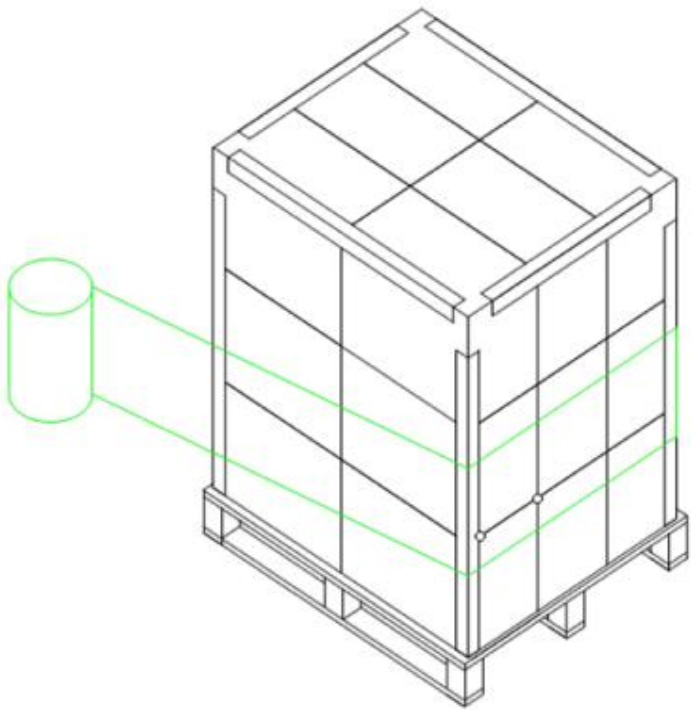
16621------生产日期，16 年 6 月 21 日，如遇双月份则用 ABC 代替

0001-----流水码

6.2 PACKING

6.2.1 PACKING FORM

A Package quantity in one Pallet: 20pcs



7. Precautions

Please pay attention to the followings when you use this TFT LCD Open Cell unit and strongly recommended to contact AUO if module process advice is required.

7.1 Mounting Precautions

(1) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the cell. And the frame on which a cell is mounted should have sufficient strength so that external force is not transmitted directly to the cell.

(2) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.

(3) You should adopt radiation structure to satisfy the temperature specification.

(3) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.

(4) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)

(5) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.

(6) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.

(7) Do not open the case because inside circuits do not have sufficient strength.

7.2 Operating Precautions

(1) The open cell unit listed in the product specification sheets was designed and manufactured for TV application

(2) The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=±200mV(Over and under shoot voltage)

(3) Response time depends on the temperature. (In lower temperature, it becomes longer.)

(4) Brightness/transmittance depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.

(5) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.

(6) When fixed patterns are displayed for a long time, remnant image is likely to occur.

(7) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interface.

7.3 Electrostatic Discharge Control

Since a open cell unit is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

7.4 Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

7.5 Storage

When storing open cell units as spares for a long time, the following precautions are necessary.

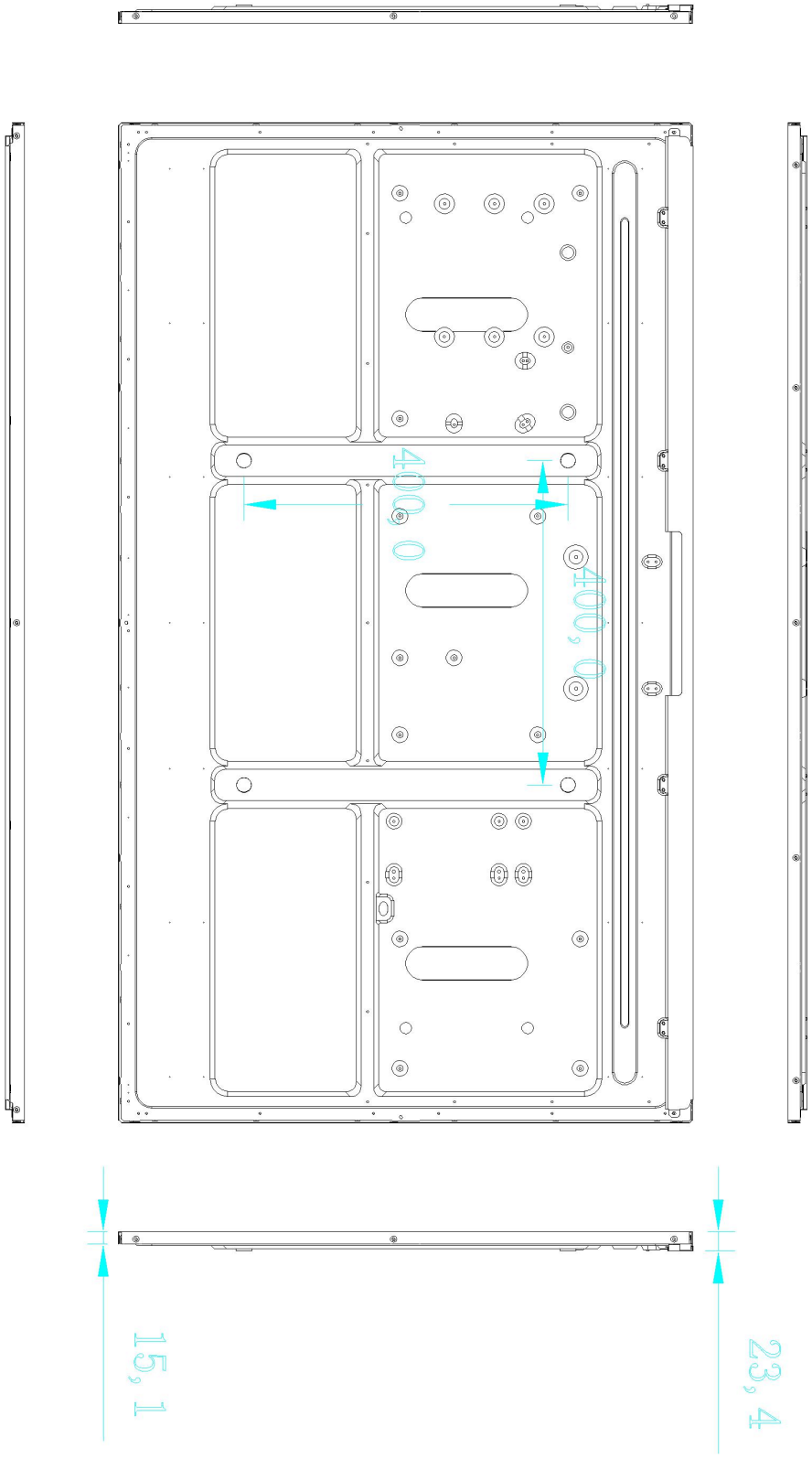
(1) Store them in a dark place. Do not expose the open cell unit to sunlight or fluorescent light. Keep the temperature between 5℃ and 35℃ at normal humidity.

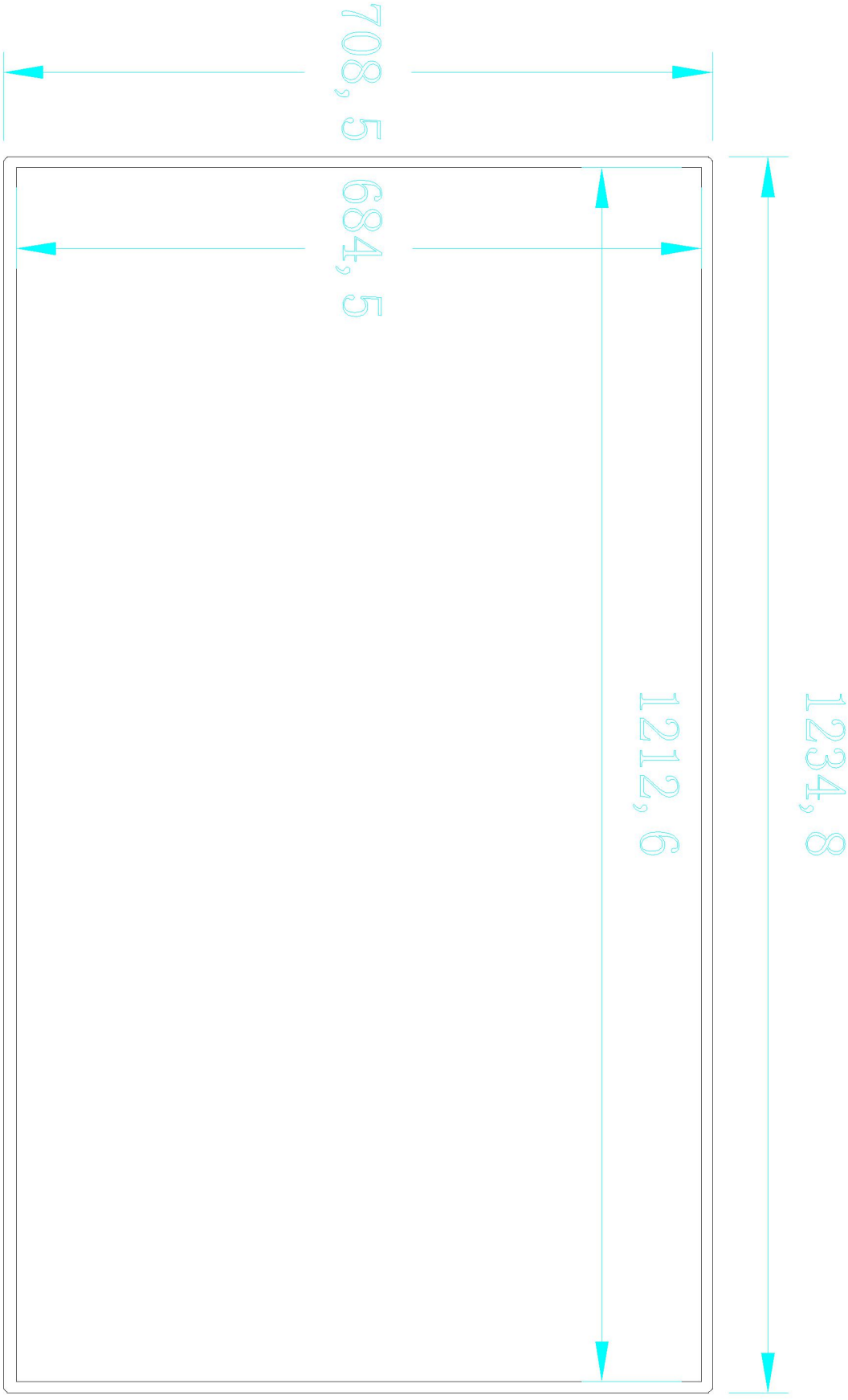
(2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

7.6 Handling Precautions for Protection Film of Polarizer

The protection film of polarizer is still attached on the surface as you receive open cell units. When the protection film is peeled off, static electricity is easily generated on the polarizer surface. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.

8. **MECHANICAL CHARACTERISTICS**




708.5
684.5
1212.6
1234.8